

1.5V Drive Pch +SBD MOSFET

TT8U1

●Structure

Silicon P-channel MOSFET / schottky barrier diode

●Features

- 1) Low On-resistance.
- 2) High Power Package.
- 3) Low voltage drive. (1.5V)

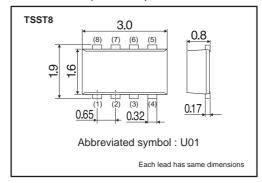
Applications

Switching

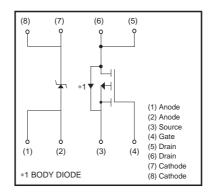
Packaging specifications

	J -	
	Package	Taping
Type	Code	TR
	Basic ordering unit (pieces)	3000
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●Dimensions (Unit:mm)



•Inner circuit



● Absolute maximum ratings (Ta=25°C)

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Parameter	Symbol	Limits	Unit	
Drain-source voltage		V _{DSS}	-20	V
Gate-source voltage		V _{GSS}	±10	V
Dunin august	Continuous	ID	±2.4	A
Drain current	Pulsed	I _{DP} *1	±9.6	A
Source current	Continuous	Is	-0.8	A
(Body diode)	Pulsed	I _{SP} *1	-9.6	A
Channel temperature		Tch	150	°C
Power dissipation		P _D *2	1.0	W / ELEMENT

^{*1} Pw≤10μs, Duty cycle≤1%

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Parameter	Symbol	Limits	Unit	
Repetitive peak reverse voltage	V _{RM}	30	V	
Reverse voltage	VR	20	V	
Forward current	l _F	1.0	A	
Forward current surge peak	I _{FSM} *1	3.0	A	
Junction temperature	Tj	150	°C	
Power dissipation	P _D *2	1.0	W / ELEMENT	

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Parameter	Symbol	Limits	Unit	
Total power dissipation	P _D *	1.25	W / TOTAL	
Range of Storage temperature	Tstg	-55 to +150	°C	

^{*} Mounted on a ceramic board

^{*2} Mounted on a ceramic board

^{*1 60}Hz / 1Cycle *2 Mounted on a ceramic board

●Electrical characteristics (Ta=25°C)

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Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	_	-	±100	nA	V _{GS} =±10V, V _{DS} =0V
Drain-source breakdown voltage	$V_{(BR)\;DSS}$	-20	_	_	V	I _D = -1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	_	-	-1	μΑ	V _{DS} = -20V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	-0.3	-	-1.0	V	V _{DS} = -10V, I _D = -1mA
		_	80	105	mΩ	I _D = -2.4A, V _G S= -4.5V
Static drain-source on-state	R _{DS (on)} *	-	105	140	mΩ	I _D = -1.2A, V _G S= -2.5V
resistance	11DS (0II)	_	150	225	mΩ	I _D = -1.2A, V _G s= -1.8V
		_	180	360	mΩ	I _D = -0.5A, V _G S= -1.5V
Forward transfer admittance	Y _{fs} *	2.4	-	_	S	V _{DS} = -10V, I _D = -2.4A
Input capacitance	Ciss	-	850	_	pF	V _{DS} = -10V
Output capacitance	Coss	_	60	_	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	_	50	_	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	_	9	_	ns	V _{DD} ≒ −10V
Rise time	tr *	_	25	_	ns	Vgs= -4.5V Ip= -1.2A
Turn-off delay time	t _{d (off)} *	-	55	_	ns	ID= -1.2A Rι ≒8.3Ω
Fall time	t _f *	_	45	_	ns	R _G =10Ω
Total gate charge	Qg *	-	6.7	_	nC	V _{DD} ≒-10V
Gate-source charge	Qgs *	_	1.7	_	nC	Vgs=-4.5V Ip=-2.4A
Gate-drain charge	Q _{gd} *	1	0.6	-	nC	$R_L = 4.2\Omega / R_G = 10\Omega$

^{*}Pulsed

<MOSFET> Body diode (source-drain)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp*	_	-	-1.2	V	I _S = -2.4A, V _{GS} =0V
*Pulsod						

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Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage drop	VF	_	0.37	0.41	V	I _F = 1.0A
Reverse leakage	IR	_	_	500	μΑ	V _R =20V

•Electrical characteristics curves

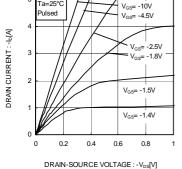


Fig.1 Typical output characteristics(I)

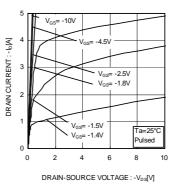


Fig.2 Typical output characteristics(${\rm I\hspace{-.1em}I}$)

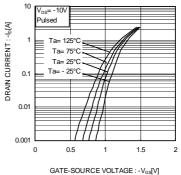


Fig.3 Typical Transfer Characteristics

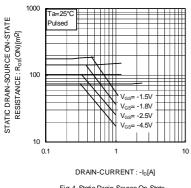


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(I)

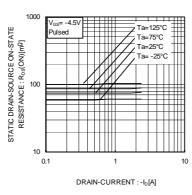


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current(II)

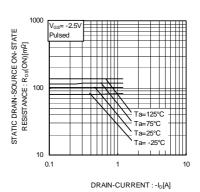


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current(Ⅲ)

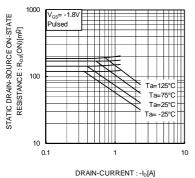


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current(IV)

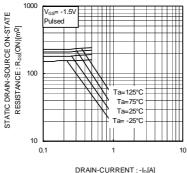


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current(V)

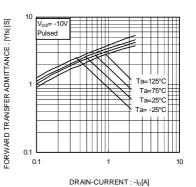
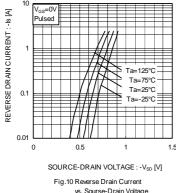
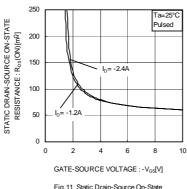
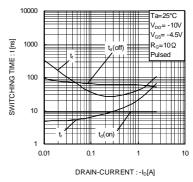


Fig.9 Forward Transfer Admittance vs. Drain Current

TT8U1 Data Sheet



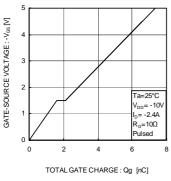


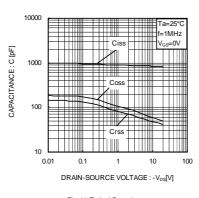


vs. Sourse-Drain Voltage

Fig.11 Static Drain-Source On-State Resistance vs. Gate Source Voltage

Fig.12 Switching Characteristics





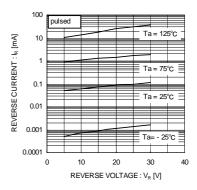


Fig.13 Dynamic Input Characteristics

Fig.14 Typical Capacitance vs. Drain-Source Voltage

Fig.15 Reverse Current vs. Reverse Voltage

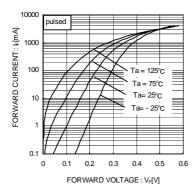


Fig.16 Forward Current vs. Forward Voltage

Measurement circuits

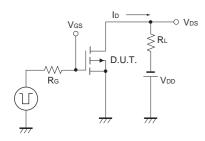


Fig.1-1 Switching Time Measurement Circuit

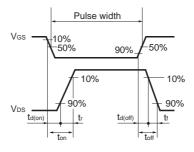


Fig.1-2 Switching Waveforms

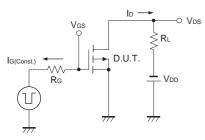


Fig.2-1 Gate Charge Measurement Circuit

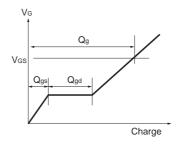


Fig.2-2 Gate Charge Waveform

Notice

1. SBD has a large reverse leak current compared to other type of diode. Therefore; it would raise a junction temperature, and increase a reverse power loss. Further rise of inside temperature would cause a thermal runaway.

This built-in SBD has low VF characteristics and therefore, higher leak current. Please consider enough the surrounding temperature, generating heat of MOSFET and the reverse current.

2. This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

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