

8-Bit, 2-Port Bus Switch

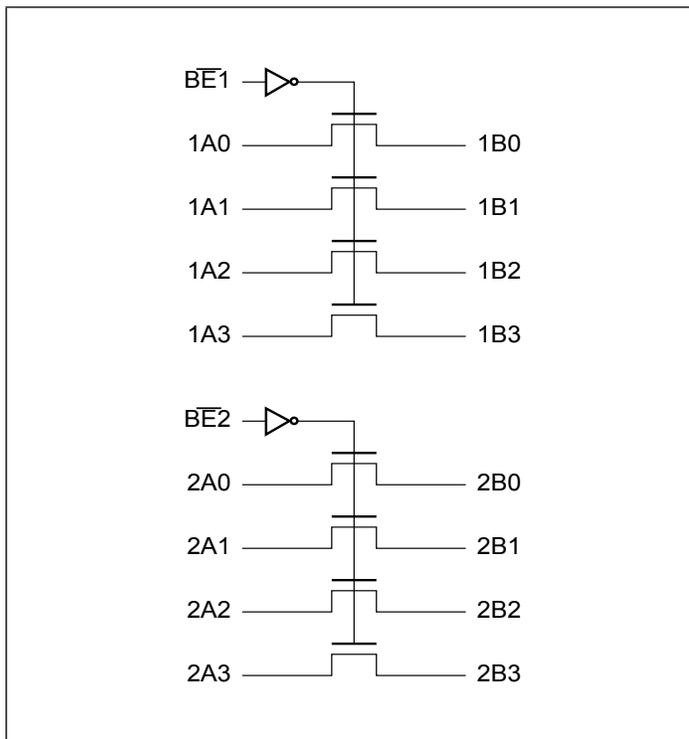
Features

- Near-Zero propagation delay
- 5-ohm switches connect inputs to outputs
- Direct bus connection when switches are ON
- Ultra Low Quiescent Power (0.2µA typical)
 - Ideally suited for notebook applications
- Pin compatible with 74 Series 244 logic devices
- Packaging (Pb-free & Green):
 - 20-pin 173-mil wide plastic TSSOP (L)
 - 20-pin 150-mil wide plastic QSOP (Q)

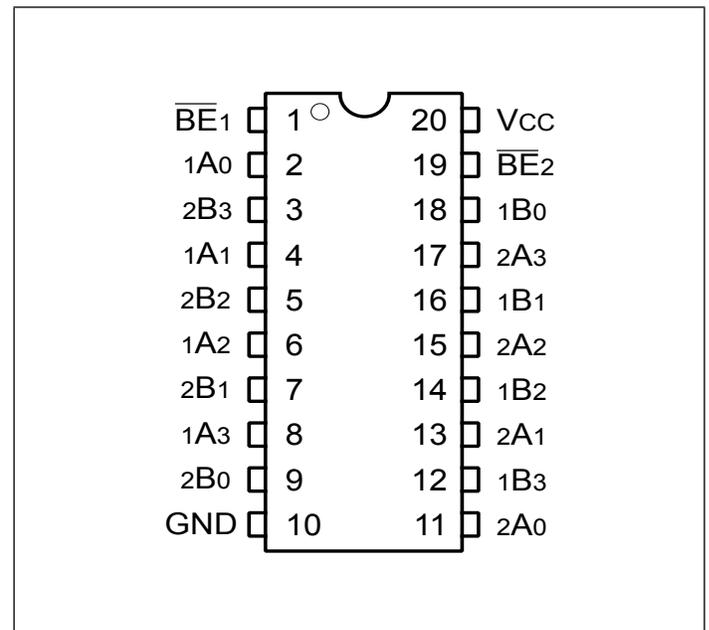
Description

Pericom Semiconductor's PI5C3244 features a set of eight bus switches which is pinout and function compatible with the PI74F-CT244T, 74F244, and 74ALS/AS/LS 244 8-bit drivers. Two enable signals ($\overline{BE}n$) turn the switches on similar to the enable signals of the 244. The bus switch creates no additional propagation delay or ground bounce noise.

Block Diagram



Pin Configuration



Truth Table⁽¹⁾

$\overline{BE}1$	$\overline{BE}2$	1A, 1B	2A, 2B
H	H	Disconnect	Disconnect
L	H	1A = 1B	Disconnect
H	L	Disconnect	2A = 2B
L	L	1A = 1B	2A = 2B

Note: 1. H = High Voltage Level
L = Low Voltage Level
Hi-Z = High Impedance

Pin Description

Pin Name	Description
$\overline{BE}n$	Bus Enable Input (Active LOW)
A0-A3	Bus A
B0-B3	Bus B
GND	Ground
VCC	Power

Absolute Maximum Ratings

Parameter	Min.	Max.	Units
Storage Temperature	-65	150	°C
Ambient Temperature with Power Applied	-40	85	°C
Supply Voltage to Ground Potential (Inputs & V _{CC} Only)	-0.5	7.0	V
Supply Voltage to Ground Potential (Outputs & D/O Only)	-0.5	7.0	V
DC Input Voltage	-0.5	7.0	V
DC Output Current	-	120	mA
Power Dissipation	-	0.5	W

Stress beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device.

DC Electrical Characteristics (Over the Operating Range, T_A = -40°C to +85°C, V_{CC} = 5V ±5%)

Parameters	Description	Test Conditions ⁽¹⁾	Min	Typ ⁽²⁾	Max	Units
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	V
I _{IH}	Input HIGH Current	V _{CC} = Max., V _{IN} = V _{CC}			±1	μA
I _{IL}	Input LOW Current	V _{CC} = Max., V _{IN} = GND			±1	μA
I _{OZH}	High Impedance Output Current	0 ≤ A, B ≤ V _{CC}			±1	μA
V _{IK}	Clamp Diode Voltage	V _{CC} = Min, I _{IN} = -18mA	-0.7	-1.2		V
I _{OS}	Short Circuit Current ⁽³⁾	A (B) = 0V, B (A) = V _{CC}	100			mA
V _H	Input Hysteresis at Control Pins			150		mV
R _{ON}	Switch On Resistance ⁽⁴⁾	V _{CC} = Min, V _{IN} = 0.0V, I _{ON} = 48 mA		5	7	Ω
		V _{CC} = Min, V _{IN} = 2.4V, I _{ON} = 15 mA		10	15	

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at V_{CC} = 5.0V, T_A = 25°C ambient and maximum loading.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.

Capacitance (T_A = 25°C, f = 1 MHz)

Parameters ⁽¹⁾	Description	Test Conditions	Typ	Units
C _{IN}	Input Capacitance	V _{IN} = 0V	6	pF
C _{OFF}	A/B Capacitance, Switch Off	V _{IN} = 0V	6	pF
C _{ON}	A/B Capacitance, Switch On	V _{IN} = 0V	12	pF

Notes:

- This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min	Typ ⁽²⁾	Max	Units
I_{CC}	Quiescent Power Supply Current	$V_{CC} = \text{Max.}$	$V_{IN} = \text{GND or } V_{CC}$		0.1	3.0	μA
ΔI_{CC}	Supply Current @ Input HIGH	$V_{CC} = \text{Max.}$	$V_{IN} = 3.4\text{V}^{(3)}$			2.5	mA
I_{CCD}	Supply Current per Input per MHz ⁽⁴⁾	$V_{CC} = \text{Max.}$ A and B Pins Open $\overline{\text{BE}}1$ or $\overline{\text{BE}}2 = \text{GND}$ Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at $V_{CC} = 5.0\text{V}$, $+25^\circ\text{C}$ ambient.
3. Per TTL driven input ($V_{IN} = 3.4\text{V}$, control inputs only); A and B pins do not contribute to I_{CC} .
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Switching Characteristics over Operating Range

Parameters	Description	Test Conditions	Com.		Units
			Min	Max	
t_{PLH} t_{PHL}	Propagation Delay ^(1,2) Ax to Bx	$C_L = 50 \text{ pF}$ $R_L = 500\Omega$		0.25	ns
t_{PZH} t_{PZL}	Bus Enable Time $\overline{\text{BEx}}$ to Ax or Bx		1.5	5.6	
t_{PHZ} t_{PLZ}	Bus Disable Time $\overline{\text{BEx}}$ to Ax or Bx		1.5	5.2	

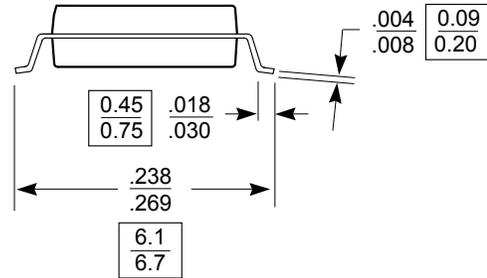
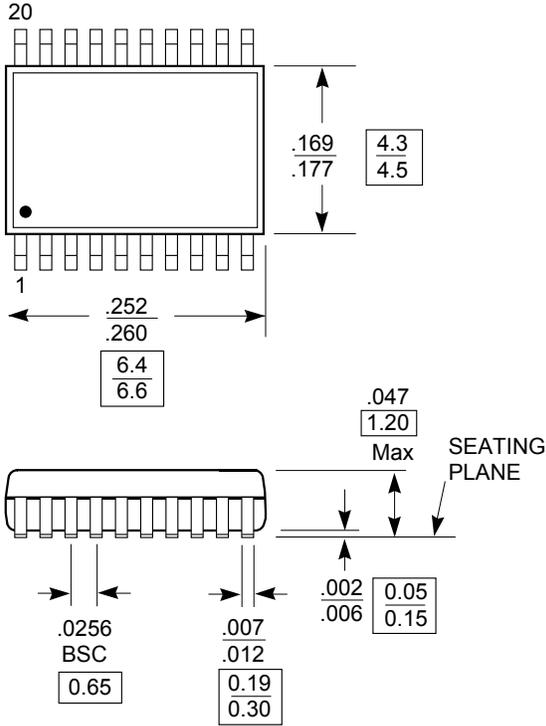
Notes:

1. This parameter is guaranteed but not tested on Propagation Delays.
2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Packaging Mechanical: 20-pin TSSOP (L20)

DOCUMENT CONTROL NO.
PD - 1311

REVISION: E
DATE: 03/09/05



- Note:**
1. Package Outline Exclusive of Mold Flash and Metal Burr
 2. Controlling dimensions in millimeters
 3. Ref: JEDEC MO-153F/AC



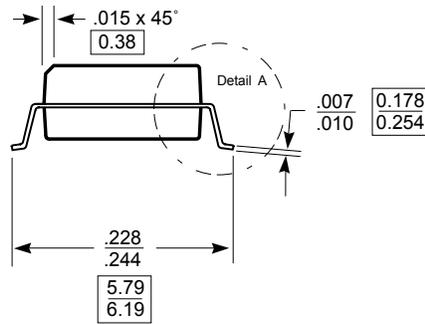
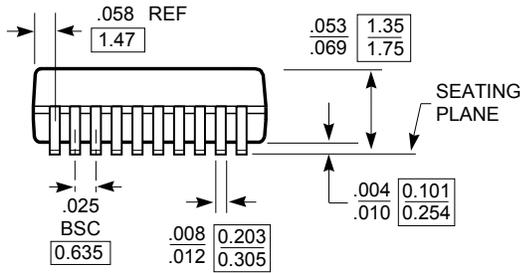
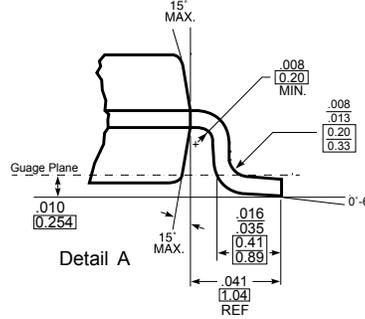
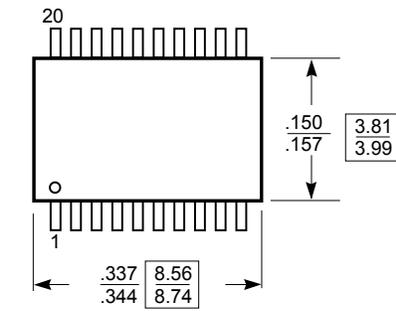
Pericom Semiconductor Corporation
3545 N. 1st Street, San Jose, CA 95134
1-800-435-2336 • www.pericom.com

DESCRIPTION: 20-Pin, 173-Mil Wide, TSSOP

PACKAGE CODE: L

Packaging Mechanical: 20-pin QSOP (Q20)

DOCUMENT CONTROL NO.
PD - 1202
REVISION: H
DATE: 10/22/07



X.XX DENOTES DIMENSIONS IN INCHES
 X.XX IN MILLIMETERS

- Note:
 1) Controlling dimensions in inches.
 2) Ref: JEDEC MO-137B/AD
 3) Dimensions do not include mold flash, protrusions or gate burrs

PERICOM
 Pericom Semiconductor Corporation
 3545 N. 1st Street, San Jose, CA 95134
 1-800-435-2335 • www.pericom.com

DESCRIPTION: 20-Pin, 150-Mil Wide, QSOP

PACKAGE CODE: Q

Ordering Information

Ordering Code	Package Code	Package Type
PI5C3244LEX	L	Pb-free & Green, 20-pin TSSOP
PI5C3244QEX	Q	Pb-free & Green, 20-pin QSOP

1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/